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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/502,117	07/21/2004	Akihiko Okubora	075834.00270	2114
33448	7590	06/30/2005	EXAMINER	
ROBERT J. DEPKE LEWIS T. STEADMAN Trexler, Bushnell, Glanlorgi, Blackstone & Marr 105 West Adams Street, Suite 3600 Chicago, IL 60603-6299			DINH, TUAN T	
			ART UNIT	PAPER NUMBER
			2841	

DATE MAILED: 06/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

EJ

Office Action Summary	Application No.	Applicant(s)	
	10/502,117	OKUBORA, AKIHIKO	
	Examiner	Art Unit	
	Tuan T. Dinh	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 July 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 July 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>07/21/04</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. **Figures 1-6** should be designated by a legend such as **--Prior Art--** because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

2. Claims 5, 7 are objected to because of the following informalities:

Claim 5, lines 2-3, it is unclear. The phrase of "a base substrate block having formed on a main side of an organic substrate a plurality of wiring layers..." is not understood and confused. Does applicant mean of "a base substrate block formed in a main side of an organic substrate, and having a plurality of wiring layers..."?

By apply art, examiner assume the phase would be read - - a base substrate block formed in a main side of an organic substrate, and having a plurality of wiring layers... - - for proper reading.

Claim 7, line 3, the phrase of "the latter form together a strip structure or microstrip structure" is not understood. What does applicant mean of "the latter" Does applicant mean "the conductive parts formed together a strip structure"?

By apply art, examiner assume the phase would be read - - the conductive parts formed together a strip structure - - for proper reading.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Kubota et al. (U.S. Patent 6,183,669).

As to claims 1, 3, Kubota et al. discloses a high-frequency module (1, column 12, line 1) as shown in figure 1 including a wiring pattern (5, column 12, line 5) formed in an organic insulative layer (2, 3) and a plurality of conductive parts (capacitor C, Inductor L, and resistor 11, column 12, lines 3-7) forming passive elements and distributed parameter elements (strip lines, column 12, line 4), which transmit a high-frequency

signal, each of the conductive parts being formed correspondingly to an area of the organic insulative layer where no woven glass fabric is laid.

As to claim 2, Kubota et al. discloses each of the conductive parts (C, I, R, and strip lines) is covered with a ground layer (5) formed on the organic insulative layer to form a strip structure or a micro-strip structure.

As to claim 4, Kubota et al. discloses the organic insulative layer is formed from any one of these organic materials having a ceramic powder dispersed therein (column 8, lines 32-42).

5. Claims 5-6, 8-12, and 14-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Geller et al. (U.S. Patent 5,929,510).

As to claim 5, 9, and 11, Geller et al. discloses a high-frequency module (10, column 2, line 10) and a method of producing a high frequency module (10) as shown in figure 1 comprising:

a base substrate block (16, 30, column 2, lines 15-16, 29-30) formed in a main side of an organic substrate (13), and having a plurality of wiring layers each including an organic insulative layer (18, 32, column 2, lines 17-18, 31-32) and a wiring pattern (22, column 2, lines 19-20) and having at least the uppermost one of the wiring layers (36) layer flattened to form a buildup surface (a surface on top portion 16 and 30), and

an elements block (40, and 42) having formed in the organic insulative layer (the insulative layer 40, and 42) formed on the main side of the buildup surface of the base substrate block (16, and 30) a wiring pattern (44, 46, 48, 50) and a plurality of

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conductive parts (62, 64, and 66, which are a resistor or capacitor, see column 2, lines 49-50) forming passive elements and distributed parameter elements (strip lines 44, 46, 48, 50, 52, 54, 56, 58, and 60), which transmit a high-frequency signal,

each of the conductive parts (components and strip lines) of the elements block (30, 40, and 42) is formed correspondingly to an area of the organic insulative layer where no woven glass fabric is laid.

As to claims 6, 12, Geller et al. discloses the base substrate block (16, 30) has a ground pattern (36, column 3, lines 1-10) in a portion of the organic insulative layer (32) corresponding to the conductive parts and no woven glass fabric is laid at least between the ground pattern and conductive parts.

As to claims 8, 14, Geller et al. discloses the wiring layers (22, 24) in the base substrate block (16, 30) have no woven glass fabric formed in portions thereof opposite to areas where the conductive parts (cap, resistor, or strip lines) are formed.

As to claims 10, and 15, Geller et al. discloses the organic substrate (13) and organic insulative layer (18, 32, 40, and 42) are formed from any one of these organic materials having a ceramic powder dispersed therein (see column 2, lines 14-15).

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 7, 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Geller et al. ('510) in view of Kamimura et al. (U.S. Patent 5,373,112).

Geller et al. does not disclose shielded by a ground pattern formed on the organic insulative layer to enclose the perimeters of the conductive parts, the conductive parts formed together a strip structure or a micro-strip structure.

Kamimura et al. shows a multilayer wiring board as shown in figures 1-3 comprising a ground layer (12, 13, column 4, lines 53-56) being shielded and enclosed the perimeters of conductive parts (1, capacitors), the conductive parts formed a strip structure

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have a ground being shield and enclosed conductive parts and the conductive parts formed a strip structure as taught by Kamimura et al, employ in the module of Geller in order to perform a grounding, and suppress noise.

Conclusion

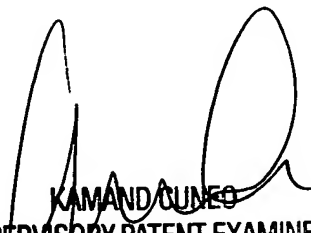
8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Ito, Moresco, and Nakatani et al. disclose related art.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan Dinh
June 24, 2005.


KAMAND CUNEO
SUPERVISORY PATENT EXAMINER
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